

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

10/574030

Appl. No. : Unassigned Confirmation No. Unassigned
 First Inventor : VAN SCHAIJK, Robertus *et al*
 Filed : Concurrently
 TC/A.U. : Unassigned
 Examiner : Unassigned

IAP5 Rec'd PCT/PTO 27 MAR 2006

Docket No. : NL03 1167 US
 Customer No. : 24738

Title: Two-Transistor Memory Cell and Method for Manufacturing

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.